

AMENDMENTS TO THE CLAIMS

1-69. (Cancelled)

70. (Cancelled)

71. (Currently Amended) ~~The apparatus as defined in claim 70, wherein the DSP~~
~~further comprises~~ A communication device, comprising:

a receiver for developing a received signal, where said receiver comprises an adaptive
device chosen from the group consisting of an equalizer, echo-canceller, adapted gain device,
and timing loop, the adaptive device having at least one operating parameter; and

a digital signal processor (DSP), where said DSP comprises:

layer one logic configured to perform OSI layer one processing;

frame check sequence logic configured to compute a frame check sequence (FCS) on
each frame of said received signal, wherein the layer one logic has access to said frame check
sequence; and

means for saving the operating parameter, if said frame check sequence indicates that
said received signal is error free.

72. (Currently Amended) The apparatus as defined in claim ~~70~~ 71, further comprising
means for using an existing parameter if said frame check sequence indicates that said received
signal contains errors.

73. (Cancelled)

74. (Currently Amended) The apparatus as defined in claim ~~70~~ 71, wherein said
frame check sequence is used to adapt a receive margin level based on said received signal.

75-79. (Cancelled)

80. (Currently Amended) The device as defined in claim ~~70~~ 71, wherein said device operates in a multipoint environment.

81. (Currently Amended) The device as defined in claim ~~70~~ 71, wherein said device operates in a half duplex environment.

82. (Currently Amended) The device as defined in claim ~~70~~ 71, wherein said device operates in a full duplex environment.

83. (Currently Amended) The device as defined in claim ~~70~~ 71, wherein said device operates in an asymmetrical duplex environment.

84-102. (Cancelled)

103. (Currently Amended) ~~The apparatus as defined in claim 70, wherein the DSP further comprises~~ A communication device, comprising:

a receiver for developing a received signal, where said receiver comprises an adaptive device chosen from the group consisting of an equalizer, echo-canceller, adapted gain device, and timing loop, the adaptive device having at least one operating parameter; and

a digital signal processor (DSP), where said DSP comprises:

layer one logic configured to perform OSI layer one processing;

frame check sequence logic configured to compute a frame check sequence on each frame of said received signal, wherein the layer one logic has access to said frame check sequence;

___ means for calculating a value for the operating parameter;

___ means for storing the calculated value as a last known good value if a message received from a remote device indicates the calculated value is acceptable;

___ means for updating the operating parameter with the last known good value if the frame check sequence indicates that said received signal is error free; and

___ means for updating the operating parameter with the most recently calculated value if the frame check sequence indicates that the received signal contains errors.

104. (Currently Amended) The apparatus as defined in claim ~~70~~ 71, wherein the DSP further comprises means for updating the operating parameter with a calculated value if said frame check sequence indicates that said received signal is error free.

105. (Currently Amended) The apparatus as defined in claim ~~70~~ 71, further comprising means for operating with an existing value for the operating parameter if said frame check sequence indicates that said received signal contains errors.

106. (Currently Amended) The apparatus as defined in claim ~~70~~ 71, wherein the means for calculating determines the value based on the frame check sequence.

107. (Previously Presented) The apparatus as defined in claim 71, wherein the DSP further comprises means for calculating the parameter value.